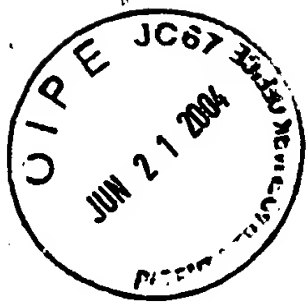


FIG. 1



Title: Method and Apparatus for Generating Sign-Off Prototypes for the Design and Fabrication of Integrated Circuits

Applicants: Raje, et al.

Docket: MONT-01307US0

Appl. No.: 09/632,494

Atty: Burt Magen

Filing Date: August 3, 2000

Phone: (415) 369-9660

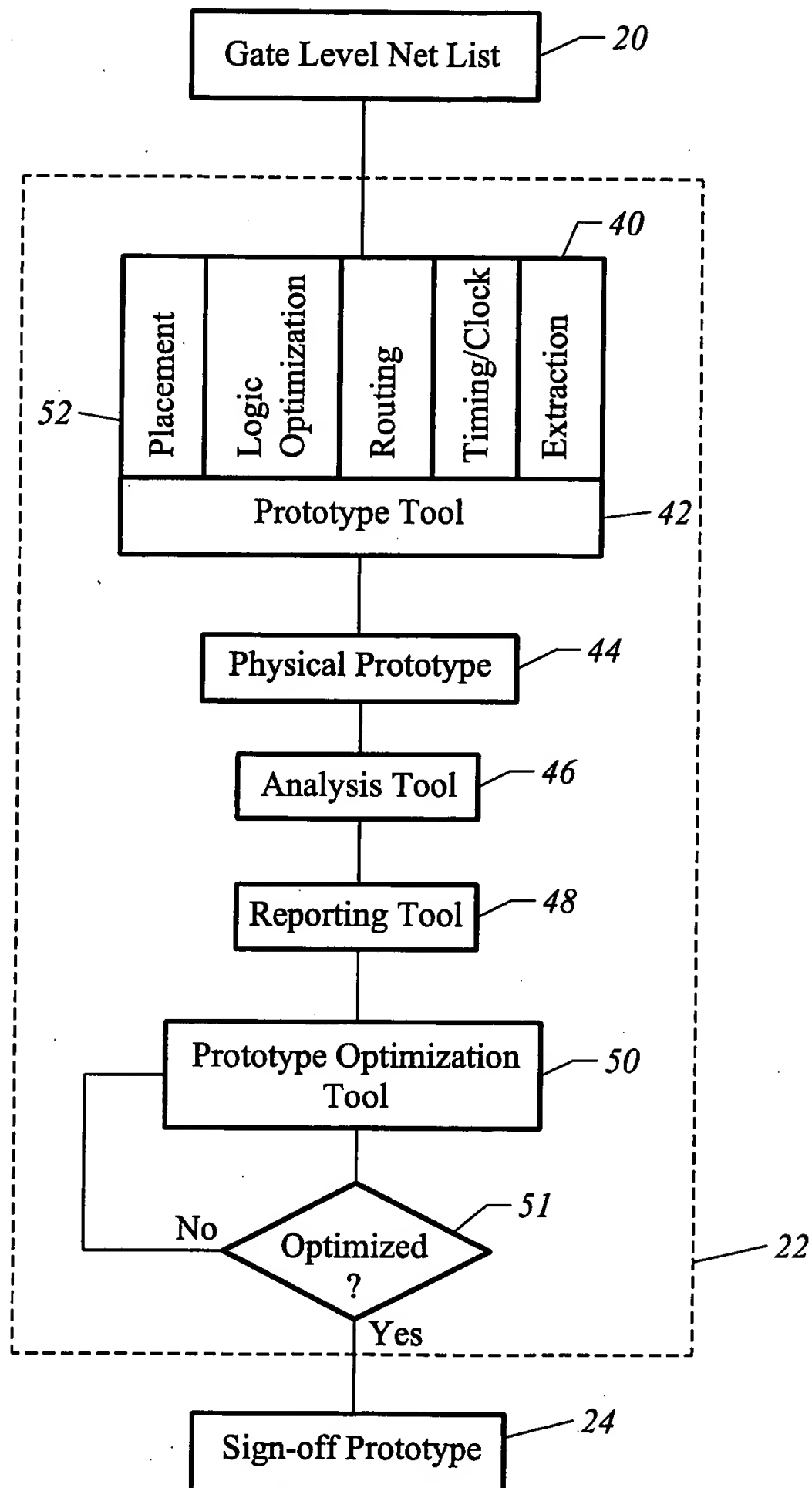


FIG. 2

Title: Method and Apparatus for Generating Sign-Off Prototypes for the Design and Fabrication of Integrated Circuits

Applicants: Raje, et al.
 Appl. No.: 09/632,494
 Filing Date: August 3, 2000

Docket: MONT-01307US0
 Atty: Burt Magen
 Phone: (415) 369-9660

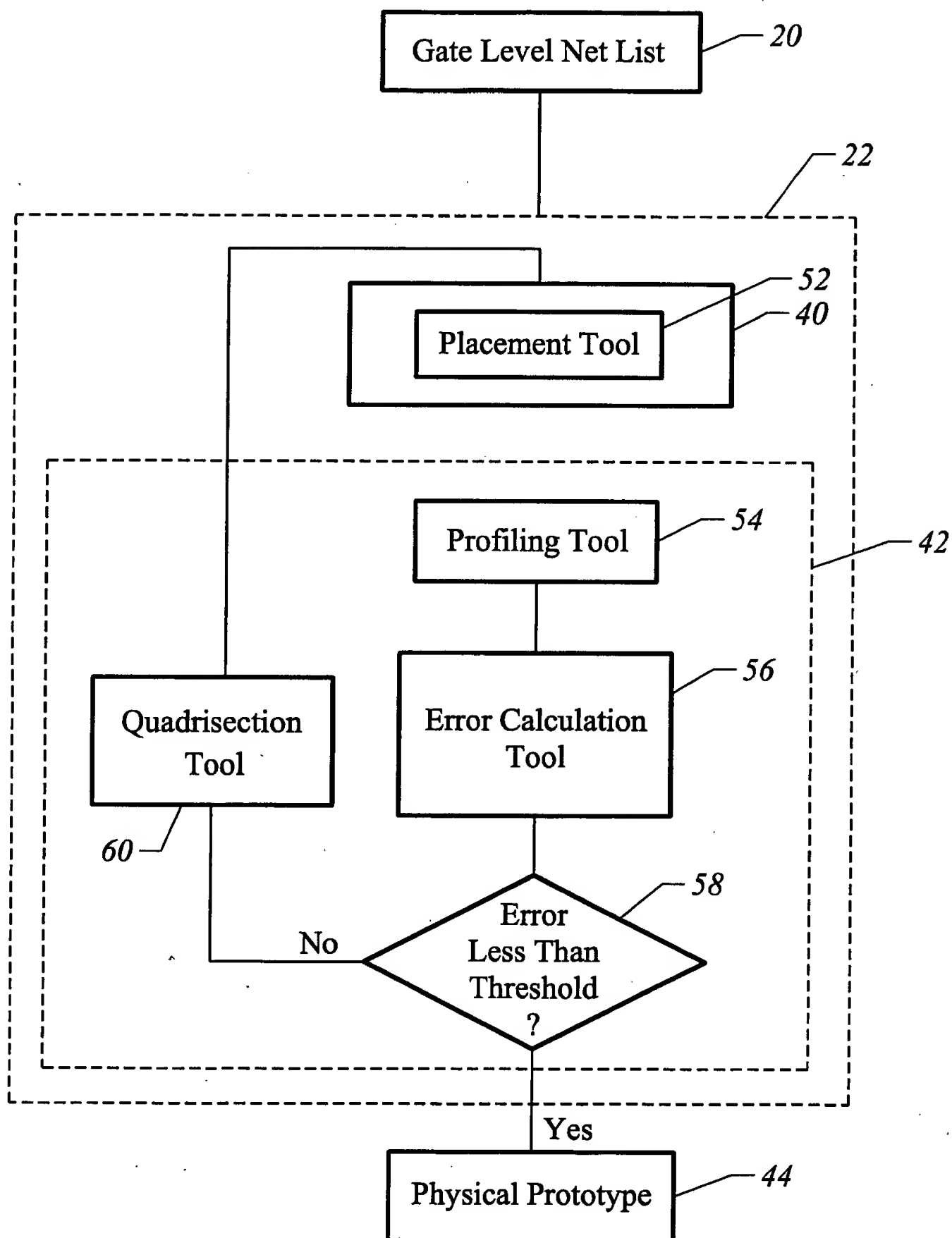
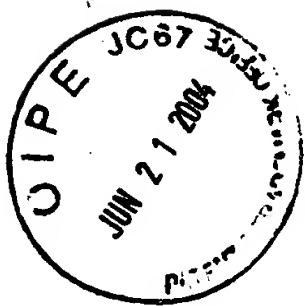
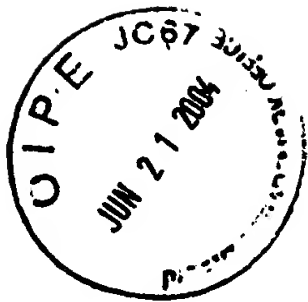


FIG. 3



Title: Method and Apparatus for Generating Sign-Off Prototypes for the Design and Fabrication of Integrated Circuits

Applicants: Raju, et al.
 Appl. No.: 09/632,494
 Filing Date: August 3, 2000

Docket: MONT-01307US0
 Atty: Burt Magen
 Phone: (415) 369-9660

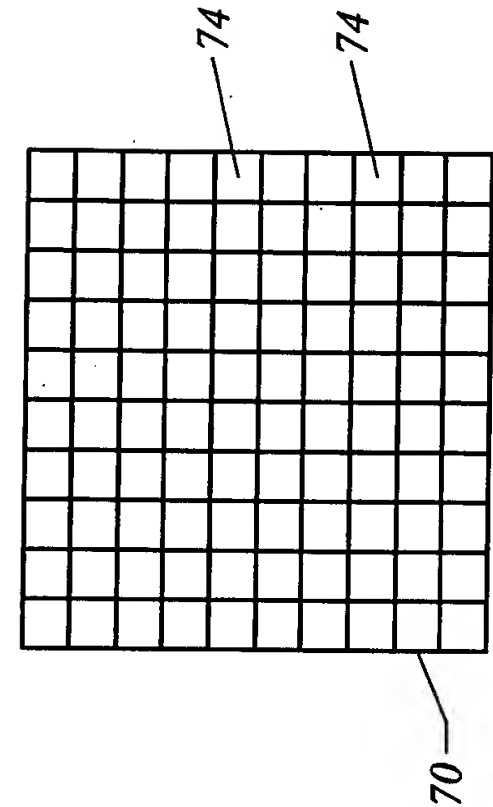


FIG. 4A

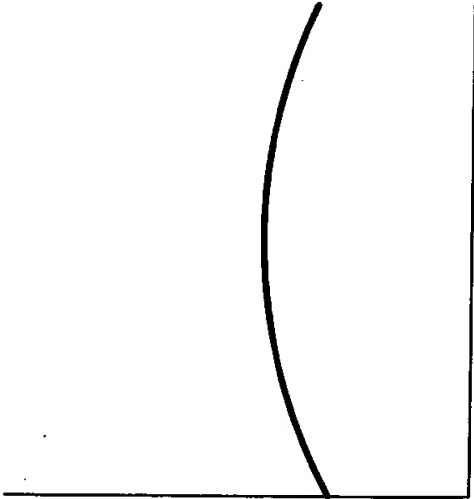
FIG. 4B

FIG. 4C

instances
of net

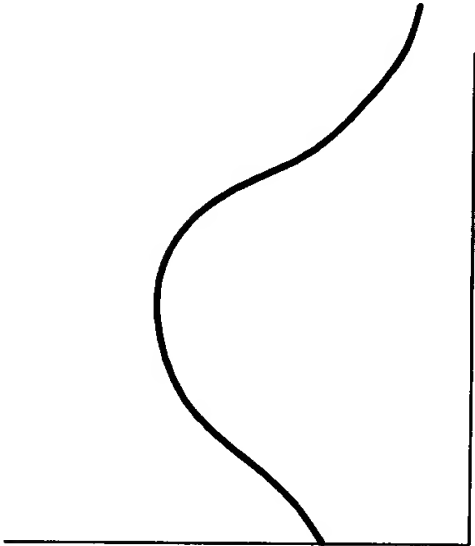
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instances
of net



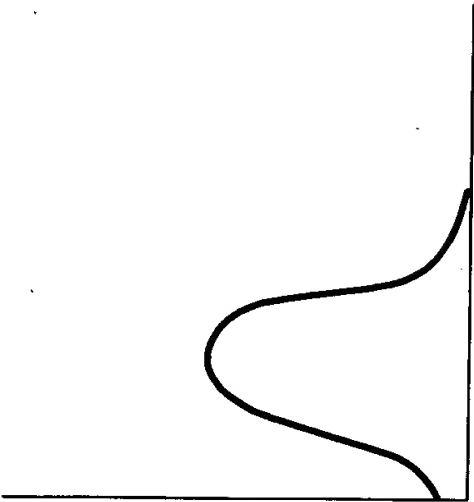
Wire length

FIG. 5A



Wire length

FIG. 5B



Wire length

FIG. 5C



Title: Method and Apparatus for Generating Sign-Off Prototypes for the Design and Fabrication of Integrated Circuits

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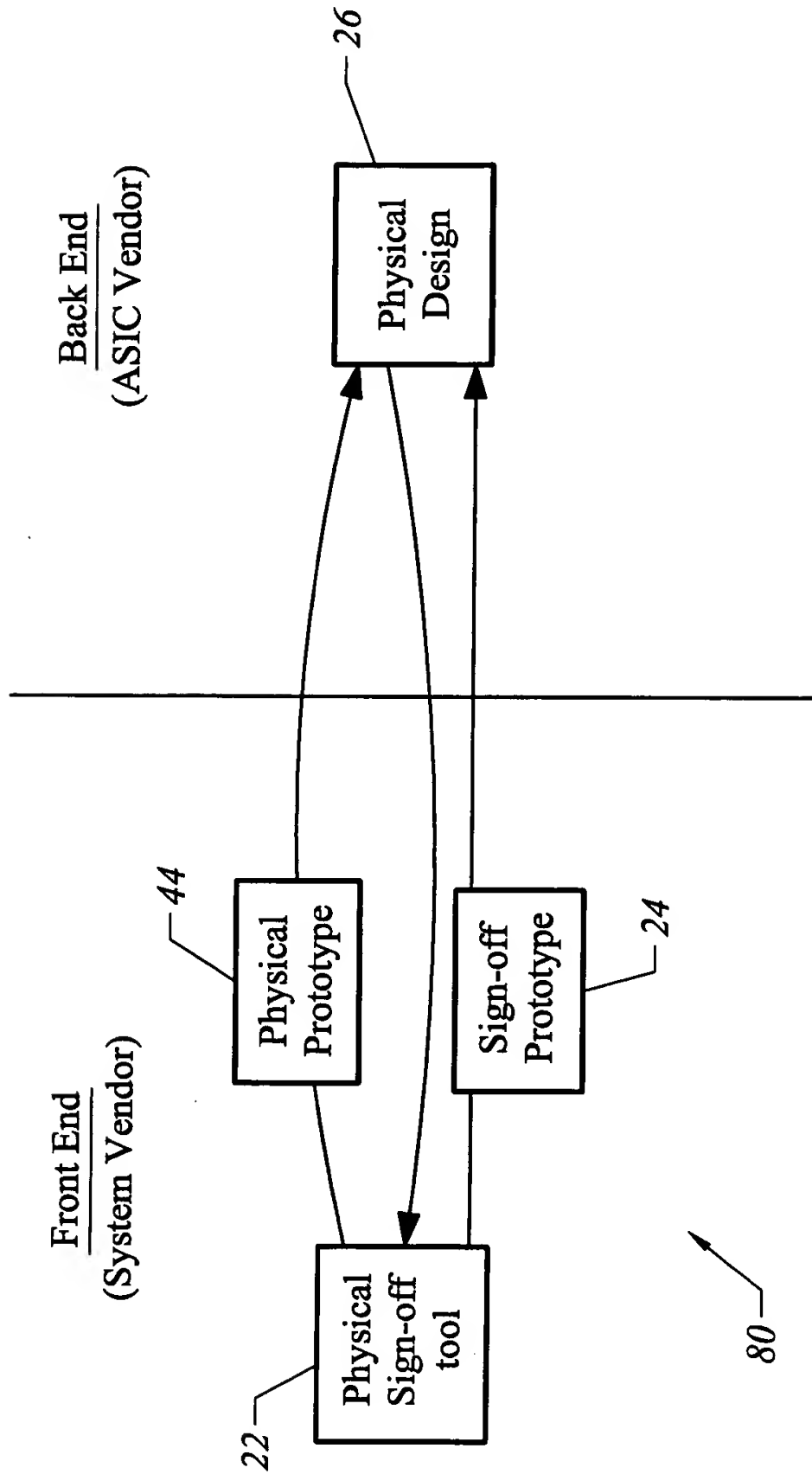


FIG. 6



Title: Method and Apparatus for Generating Sign-Off Prototypes for the Design and Fabrication of Integrated Circuits

Applicants: Raje, et al.

Docket: MONT-01307US0

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Atty: Burt Magen

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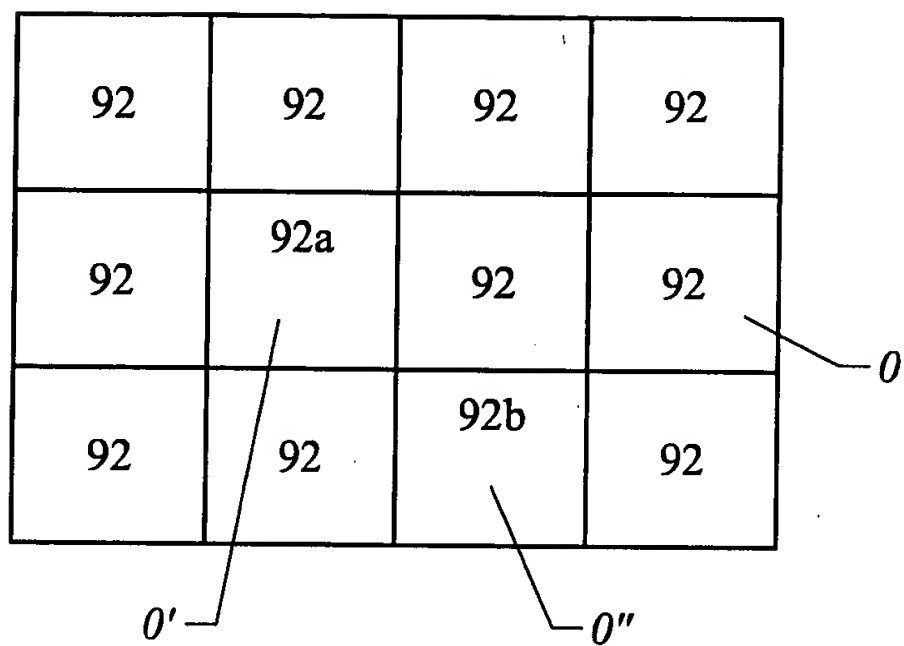


FIG. 7

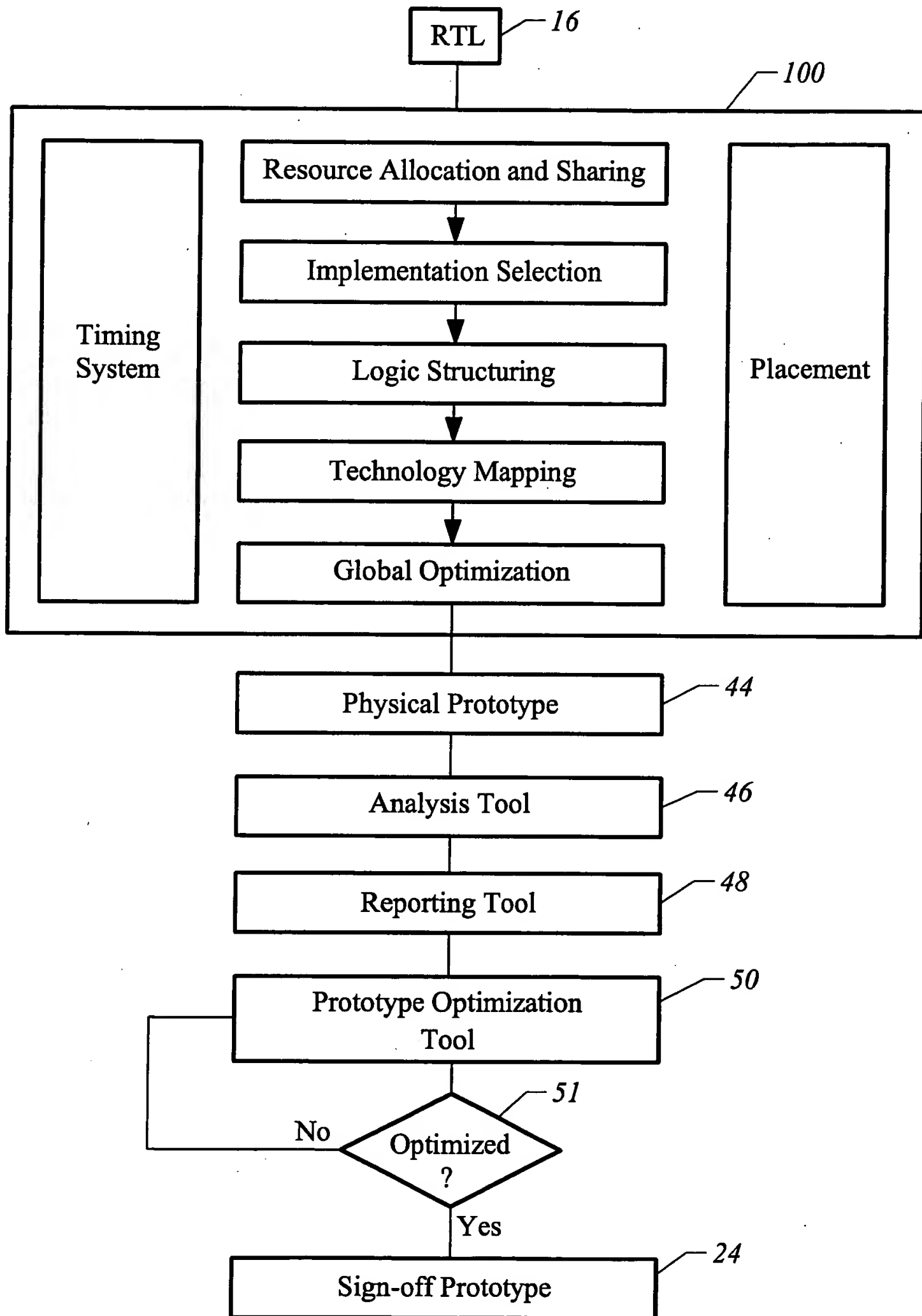


FIG. 8

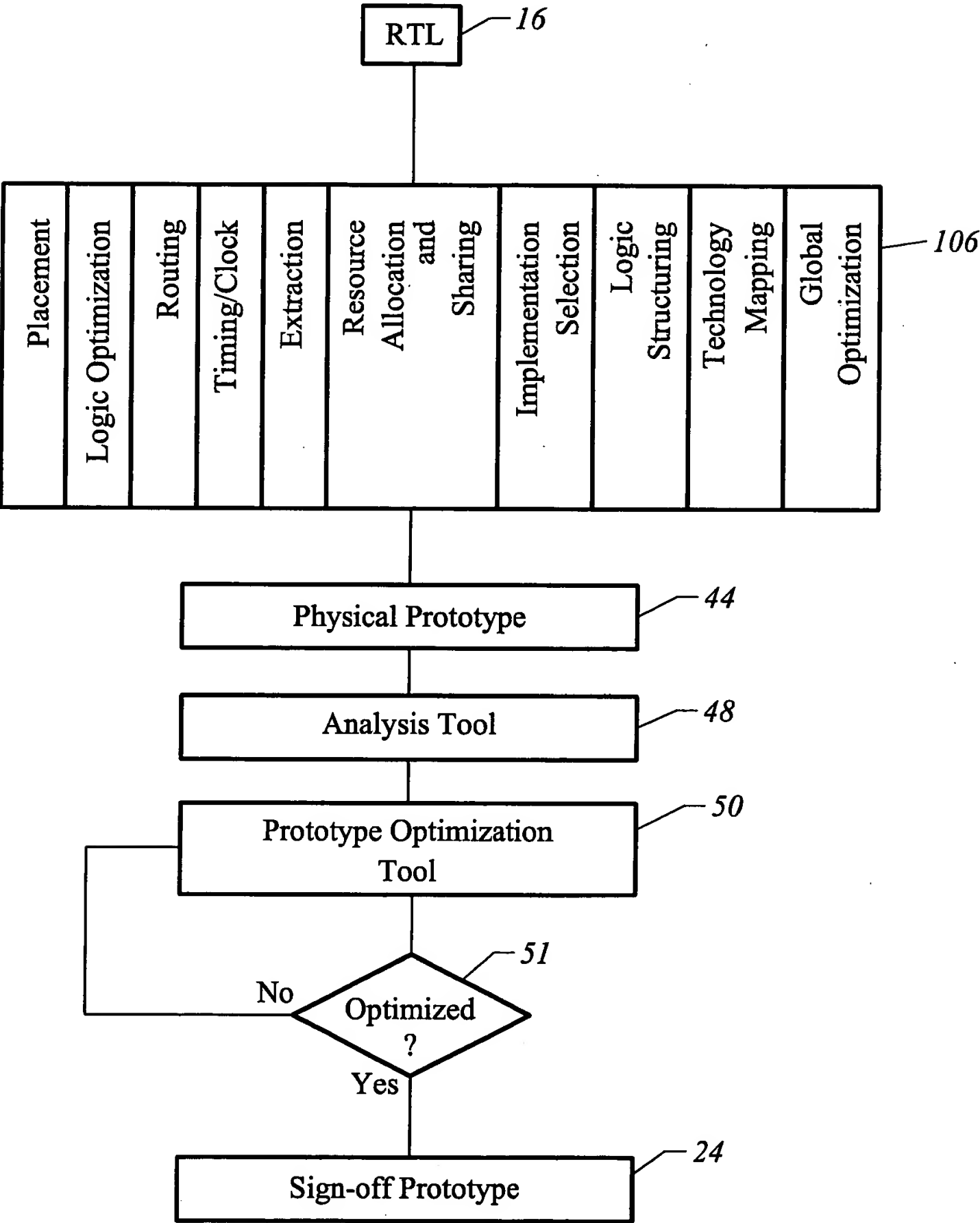


FIG. 9